

Prénom : Wenceslas

Nom : RAHAJANDRAIBE

Professeur des Universités - section CNU 63

Liste publications 2012-2018

- [R1] Samir A., Kussener E., Rahajandraibe W., Girardeau L., Bert Y., Barthélémy H., "A sub-1-V, high precision, ultra low-power, process trimmable, resistorless voltage reference with low cost 90-nm standard CMOS technology," *Analog Integr. Circ. Sig. Process.* ISSN: 0925-1030, Dec 2012 vol. 73 n°3 pp. 693–706 doi: 10.1007/s10470-012-9852-5
- [R2] Marzaki A., Bidal V., Laffont R., Rahajandraibe W., Portal J-M., Bouchakour R., "PSP Based DCG-FGT transistor Model Including Characterization Procedure on dummy cell," *Journal of Energy and Power Engineering*, ISSN 1934-8975, USA, Nov. 2012, vol. 6, n°11, pp 1878-1883.
- [R3] Marzaki A., Bidal V., Laffont R., Rahajandraibe W., Portal J-M., Bergeret E, Bouchakour R. "On the Investigation of a Novel Dual-Control-Gate Floating Gate Transistor for VCO Applications," *Bulletin of Electrical Engineering and Informatics*, Vol. 2 n°3 (ISSN 2089-3191, e-ISSN: 2302-9285), 2013.
- [R4] Marzaki A., Bidal V., Laffont R., Rahajandraibe W., Portal J-M., Bouchakour R., "Migration from 130 nm EEPROM technology to 90 nm Flash technology of the Dual-Control-Gate Floating Gate Transistor," in *IERI Journals - Advances in Mechanical Engineering* (ISSN: 2160-0619), 2013.
- [R5] Castellani-Coulié K., Aziza H., Rahajandraibe W., Micolau G., Portal J-M., "Development of a CMOS Oscillator Concept for Particle Detection and Tracking," *IEEE Transactions on Nuclear Sciences*, (No. TNS-00747-2012.R1). vol. 60 n°4, pp. 2450-2455, Aug. 2013 doi: 10.1109/TNS.2013.2254723
- [R6] Rahajandraibe, W.; Haddad, F.; Aziza, H.; Castellani-Coulié, K.; Portal, J.-M., "Low Power RF Transceiver with Built-In-Tuning of the Local Oscillator for Open Loop Modulation," in *Journal of Low Power Electronics - JOLPE* Vol. 10 N° 1 March 2014. March 2014. pp.173-181(9) <http://dx.doi.org/10.1166/jolpe.2014.1308>
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- [R8] S. Ben Krit, W. Rahajandraibe, K. Coulié-Castellani, G. Micolau, A. Lyoussi, "First Investigations on the Feasibility of Integration of a Readout System for Neutrons Detection in Harsh Environment", *IEEE Transactions on Nuclear Science*, vol. 61, no. 4, pp. 2271-2278, August, 2014. doi: 10.1109/TNS.2014.2340031
- [R9] I. Barraj, H. Trabelsi, W. Rahajandraibe, M. Masmoudi, "An Energy-Efficient Tunable CMOS UWB Pulse Generator," *Springer BioNanoScience*, June 2015, Volume 5, Issue 2, pp 117-122. doi: 10.1007/s12668-015-0167-8
- [R10] I. Barraj, H. Trabelsi, W. Rahajandraibe, M. Masmoudi, "Modular baseband pulse generator for IR-UWB transmitter," *IET Electronics Letters* 2015. Vol. 51, n°19, pp. 1550-1552. doi: 10.1007/s12668-015-0167-8
- [R11] Borrel N., Champeix C. , Kussener E. , Rahajandraibe W. , et al., "Electrical model of an inverter body-biased structure in triple-well technology under pulsed photoelectric laser stimulation," *Microelectronic Reliability*, vol. 55, Issues 9-10, pp. 1–8, 2015. doi:10.1016/j.microrel.2015.06.144

- [R12] E. Ali, C. Hangmann, C. Hedayat, F. Haddad, W. Rahajandraibe and U. Hilleringmann, "Event Driven Modeling and Characterization of Second Order Voltage Switched CP-PLL", IEEE Transactions on circuits and systems I: Regular papers TCAS-I, vol.63, No. 99, pp.1-12, Jan 2016. doi: 10.1109/TCSI.2015.2512759
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- [R14] I. Ghorbel, F. Haddad, W. Rahajandraibe and M. Loulou, "A Subthreshold Low-Power CMOS LC-VCO with High Immunity to PVT Variations", Analog Integrated Circuits and Signal Processing – Springer, pp. 1-12, 2017. DOI: 10.1007/s10470-017-1047-7
- [R15] I. Ghorbel, F. Haddad, W. Rahajandraibe, "Design Methodology of Ultra-Low-Power LC-VCOs for IoT applications" Journal of Circuits, Systems, and Computers, 2018 Ref.: Ms. No. WSPC-JCSC-D-18-00134R1

Brevet

- [B1] Rahajandraibe W., Meillère S., Kussener E., Barthélemy H. "Resistive Device Comprising a Silicon-Nanowire Comprising Strain Gauge and Method for Optimizing the Electrical Consumption of such a Device" Brevet CNRS/USTV – Extension Internationale WO2013001081, June, 29, 2012. <http://patentscope.wipo.int/search/en/WO2013001081>
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- [B4] Rahajandraibe W., Aziza H., Coulié-Castellani K., Micolau G. "Device and Method for Detecting Radiation Particles US Patent US9921317 20 mars 2018 <https://patents.google.com/patent/US9921317B2/en>

Licence

- [L1] Rahajandraibe W., Meillère S., Kussener E., Barthélemy H. "Dispositif résistif à jauge de contrainte à nanofils de silicium et procédé d'optimisation de la consommation électrique d'un tel dispositif " Mention de la délivrance faite au Bulletin officiel de la propriété industrielle n° 13/32 du 09.08.13 (n° de publication 2 977 918).

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- [C53] Castellani-Coulié, K.; Aziza, H.; Rahajandraibe, W.; Micolau, G.; Portal, J.-M., "Investigation of a CMOS Oscillator Concept for Particle Detection and Diagnosis," 13th IEEE Latin American Test Workshop (LATW 2012), Quito (Ecuador), 10-13 April 2012, pp.1-5 DOI: 10.1109/LATW.2012.6261247
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- [C61] Marzaki, A.; Bidal, V.; Laffont, R.; Rahajandraibe, W.; Portal, J.M.; Bouchakour, R, "A Dual-Control-Gate Floating Gate Transistor Migration From 130 nm EEPROM technology to 90 nm Flash technology," 11th IEEE International Conference on Solid Dielectrics - June 30th - July 4th 2013.
- [C62] Savary, E; Nebhen, J.; Rahajandraibe, W.; Dufaza, C.; Meillere, S.; Kussener, E.; Barthélemy, H.; "Readout Electronic for Digital Output Resistive NEMS Audio Sensor," IEEE DCIS, Doubaï, June 9-12, 2013.
- [C63] Ali, E. ; Rahajandraibe, W. ; Haddad, F. ; Hedayat, C. ; Hangmann, C. ; "Simulative Characterization of the Stability for Second Order Voltage Switched CP-PLL," 56th IEEE Intl. Midwest Symposium on Circuits & Systems, Columbus, Ohio from August 4-7, 2013, pp. 153-156 10.1109/MWSCAS.2013.6674608
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- [C67] Marzaki A., Bidal V., Laffont R., Rahajandraibe W., Portal J-M., Bouchakour R., "Study of comparison between the DCG-FGT and its equivalent circuit in MOS technology," in 13rd International Symposium on Communication and Information Technology, (ISCIT 2013). pp. 248-252
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DOI: 10.1109/DTIP.2014.7056676
- [C72] Ben Krit, S ; Coulie-Castellani, K.; Rahajandraibe, W.; Micolau, G.; Lyoussi, A., "Investigations of margins for the interplay using a digital testing tool dedicated to neutrons discrimination," IEEE International Conference on Electronics, Circuits, and Systems (ICECS), Marseille, France, December 7-10, 2014, pp. 806-809 DOI: 10.1109/ICECS.2014.7050108
- [C73] Savary E.; Rahajandraibe W.; Meillère S.; Kussener E.; Barthélemy H.; Czarny J.; Lhermet H.; Robert P. "High resolution NEMS smart audio sensor based on resistive silicon nano wires for hearing aids". IEEE International Conference on Electronics, Circuits, and Systems (ICECS), Marseille, France, December 7-10, 2014. pp. 558-561 doi: 10.1109/ICECS.2014.7050046
- [C74] N. Borrel, C. Champeix, M. Lisart and A. Sarafianos, E. Kussener, W. Rahajandraibe, J-M. Dutertre "Characterization and Simulation of a Body Biased Structure in Triple-Well Technology Under Pulsed Photoelectric Laser Stimulation", International Symposium for Testing and Failure Analysis (ISTFA 2014) November 9-13, 2014, Houston, TX, USA
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- [C76] E. Ali, W. Rahajandraibe, F. Haddad, N. Tall, C. Hangmann and C. Hedayat, "Simulation and Validation of Arbitrary Ordered VSCP-PLL Using Event-Driven Macro-Modeling", IEEE Int. Symposium on Circuits and Systems (ISCAS), pp. 878 – 881, Lisbon, Portugal, May 2015. doi: 10.1109/ISCAS.2015.7168774
- [C77] E. Ali, W. Rahajandraibe, F. Haddad, N. Tall, C. Hangmann and C. Hedayat, "Modeling & PVT Characterization of arbitrary ordered VSCP- PLL using an Efficient Event-Driven Approach", IEEE New Circuits and Systems 2015, pp. 1-4, Grenoble, France, June 2015. doi :10.1109/NEWCAS.2015.7182081
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- [C79] I. Ghorbel, F. Haddad and W. Rahajandraibe, "Ultra Low Power RF Cross-Coupled VCO Design in the Subthreshold Regime with High Immunity to PVT Variations in 130nm CMOS technology", IEEE New Circuits and Systems 2015, pp. 1-4, Grenoble, France, June 2015. doi: 10.1109/NEWCAS.2015.7182034
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- [C82] Karine Coulié-Castellani, Wenceslas Rahajandraibe, Hassen Aziza, Jean Michel Portal, Gilles Micolau: "Improvement of a detection chain based on a VCO concept for microelectronic reliability under natural radiative environment." IEEE Latin Area Test Symposium – Mars 2015 pp. 1-5 <http://dx.doi.org/10.1109/LATW.2015.7102513>
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- [C85] W. Rahajandraibe, F. Haddad, H. Aziza, K. Castellani-Coulié, J-M. Portal: "Low cost built-in-tuning of on-chip passive filters for low-IF double quadrature RF receiver," IEEE Latin Area Test Symposium – Mars 2015, 1-4 <http://dx.doi.org/10.1109/LATW.2015.7102501>
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[C89] Andrianjohany N., Pourrouquet P., Coulie K., Chatry N., Rahajandraibe W., Standarovski D., “Prediction methodology of Single Event Effect Sensitivity and application on SRAM device,” IEEE European Conference on Radiation and Its Effects on Components and Systems (RADECS), Bremen, Germany, September 19-23 2016. DOI à paraître

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[C94] K. Coulié, W. Rahajandraibe, H. Aziza, G. Micolau, R. Vauché, “Detection limit of a VCO based detection chain dedicated to particles recognition and tracking”, EPJ Web of Conferences 170, 09002 (2018), ANIMMA 2017, <https://doi.org/10.1051/epjconf/201817009002>

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[C96] Fayrouz Haddad, Imen Ghorbel, Wenceslas Rahajandraibe, Mourad Loulou and Abdelhalim Slimane, “Current-reuse RF LC-VCO Design for Autonomous Connected Objects”, IEEE Int. Conf. Electronics, Circuits and systems (ICECS), Dec. 2018, Bordeaux, France (publication dans IEEEExplore en cours).

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- [C99] A. T. Tchoualack, J. P. Walder, W. Vervisch, L. Ottaviani, W. Rahajandraibe, A. Klix, "Front End Electronics for SiC Based Fast Neutron Sensors," EEE Nuclear Science Symposium and Medical Imaging Conference (NSS/MIC) 2018
- [C100] I. Ghorbel, F. Haddad, W. Rahajandraibe "A subthreshold low-power NMOS LC-VCO Design for Autonomous Connected Objects," IEEE Conference on Microelectronics (ICM 2018), December 16-19, 2018. (en cours d'édition)

Tutorial invité

- [TU1] Rahajandraibe W., Kussener E., Meillère S., Barthélemy H., "Wireless Sensor Networks and Nanotechnology Paradigm," invited to 2nd Radio and Antenna Days of the Indian Ocean, Mauritius (RADIO) 7th-10th April 2014.
- [TU2] Rahajandraibe W., "Investigation of low frequency noise in MEMS resistive smart sensor for wireless sportive activity and health monitoring," invited to inter-Disciplinary Underground Science and Technology (i-DUST) conferences, Apt, Rustrel, June 2014.
- [TU3] Rahajandraibe W., Kussener E., Meillère S., Barthélemy H., "Wireless Sensor Networks and Nanotechnology Paradigm," invited to 2nd Radio and Antenna Days of the Indian Ocean, Mauritius (RADIO) 7th-10th April 2014.
- [TU4] Rahajandraibe W., "Investigation of low frequency noise in MEMS resistive smart sensor for wireless sportive activity and health monitoring," invited to inter-Disciplinary Underground Science and Technology (i-DUST) conferences, Apt, Rustrel, June 2014.

Rencontres nationales – GdR

- [N1] K. Castellani-Coulié, W. Rahajandraibe, "Développement d'une chaîne de détection basée sur le principe d'un oscillateur contrôlé en tension Journées Radsol organisées par le GDR ERRATA, Paris, (France), 12-13 juin 2014.
- [N2] K. Castellani-Coulié, G. Micolau, H. Aziza, W. Rahajandraibe, J-M. Portal « Propagation d'erreurs transitoires dans les circuits et systèmes: approche systématique pour le diagnostique et l'analyse », Journées Radsol organisées par le GDR ERRATA, Paris, (France), 14-15 juin 2012. <http://gdr-errata.fr/>
- [N3] K. Castellani-Coulié, W. Rahajandraibe « Développement d'une chaîne de détection basée sur le principe d'un oscillateur contrôlé en tension, » Journées Radsol organisées par le GDR ERRATA, Paris, (France), 12-13 juin 2014. <http://gdr-errata.fr/>

Groupe de Recherche (GdR)

- Membre du GDR SOC2 « **System On Chip, Systèmes embarqués et Objets Connectés** »
- Membre GDR ERRATA : Effet des Radiations au Niveau Atmosphérique et Terrestre.

Expertise Nationale

- **Expertise de 3 thèses CIFRE pour l'ANRT (2015-2018)**
- **Expertise CIR/JEI auprès du ministère de l'enseignement supérieur Depuis 2010**

Membre de comité de conférence internationale

- Co-organisation de la conférence internationale IEEE ICECS (International Conference on Electronics Circuits and Systems) du 7-14 décembre 2014 (<http://www.ieee-icecs2014.org/>).
- Membre du Comité technique des programmes pour le *Track « Analog circuits design »* de la conférence New Circuits and Systems (IEEE NEWCAS).
- Membre du comité technique des programmes (Track Chair)
 - conf. [IEEE NEWCAS](#) 2013 – Paris
 - conf. [IEEE NEWCAS](#) 2014 – Trois Rivières
 - conf. [IEEE NEWCAS](#) 2015 – Grenoble
 - conf. [IEEE NEWCAS](#) 2017 – Strasbourg
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